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MAY 18, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/551,027	04/17/2000	Wendell P. Noble	303.379US2	1158

TITLE OF INVENTION: CIRCUIT AND METHOD FOR A FOLDED BIT LINE MEMORY CELL WITH VERTICAL TRANSISTOR AND TRENCH CAPACITOR

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Wendell P. Noble Jr. et al.

Title: CIRCUIT AND METHOD FOR A FOLDED BIT LINE MEMORY CELL WITH VERTICAL TRANSISTOR AND TRENCH CAPACITOR

Docket No.: 303.379US2 Filed: April 17, 2000 Examiner: Michael Trinh

Customer No.: 21186

Commissioner for Patents

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Customer Number: 21186

Viet V. Tong

Reg. No. 45,416

Serial No.: 09/551,027

Group Art Unit: 2822

February 18, 2004

Due Date: May 18, 2004

Confirmation No.: 1158

Notice of Allowance Date:

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LEREDITH MESCHET